

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of :

Hirofumi HARADA :

Serial No. 10/812,443 : Group Art Unit - 2823

Filed: March 30, 2004 : Examiner: Fernando L. Toledo

For: METHOD OF MANUFACTURING A:

VERTICAL MOS TRANSISTOR : Docket No. S004-5253

MS NON-FEE AMENDMENT COMMISSIONER FOR PATENTS P.O. BOX 1450 ALEXANDRIA, VA 22313-1450

## RESPONSE

S I R:

In response to the Office Action dated August 31, 2005, applicant amends his application as follows:

## ELECTION OF SPECIES:

Applicant provisionally elects the species of invention characterized by the Examiner as Species I, drawn to Figs. 3-11, and submits that claim 1 is readable on the elected species.

## MAILING CERTIFICATE ON PAGE 2